

<b>Notice of References Cited</b>	Application/Control No. 10/634,437		Applicant(s)/Patent Under Reexamination LIU ET AL.	
	Examiner Eric B. Chen		Art Unit 1765	Page 1 of 1

#### U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,776,696	08-2004	Mahulikar et al.	451/57
	B	US-6,361,712	03-2002	Honda et al.	252/79.3
	C	US-6,630,390	10-2003	Andideh et al.	438/427
	D	US-6,191,086	02-2001	Leon et al.	510/175
	E	US-6,447,563	09-2002	Mahulikar, Deepak	51/309
	F	US-6,749,488	06-2004	Pasqualoni et al.	451/41
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

#### FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

#### NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, Silicon Processing for the VLSI Era, 2002, Lattice Press, Vol. 4, pp. 338-39.
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.